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843.43311X00

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ANTONELLI, TERRY STOUT & KRAUS, LLP
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10/04/2006 RHEBRAH1 00000044 10726507

01 FC:1501 1400.00 OP
02 FC:1504 300.00 OP
03 FC:8001 12.00 OP

Ricardo E. Perez (Depositor's name)
Ricardo Perez (Signature)
10/03/2006 (Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/726,507	12/04/2003	Digh Hisamoto	843.43311X00	7172

TITLE OF INVENTION: FABRICATION METHOD AND STRUCTURE OF SEMICONDUCTOR NON-VOLATILE MEMORY DEVICE

APPL. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE (\$)	DATE DUE
Nonprovisional	NO	\$1400	\$300	\$1700	10/03/2006

EXAMINER	ART UNIT	CLASS-SUBCLASS
FENTY, JESSE A	2815	257-407000

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PLEASE NOTE: Unless an assignee is identified below, no assignee data will appear on the patent. If an assignee is identified below, the document has been filed for recordation as set forth in 37 CFR 3.11. Completion of this form is NOT a substitute for filing an assignment.

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RENESAS TECHNOLOGY CORP.

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

TOKYO, JAPAN

Please check the appropriate assignee category or categories (will not be printed on the patent): ☐ Individual ☒ Corporation or other private group entity ☐ Government

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Date: OCTOBER 3, 2006

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Larry N. Anagnos

Registration No. 32,392

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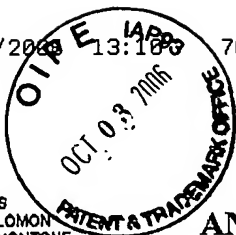
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FACSIMILE TRANSMITTAL SHEET
OCTOBER 3, 2006

TO: Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

RE: U.S. Patent Application No. **10/726,507**
For: **FABRICATION METHOD AND STRUCTURE OF SEMICONDUCTOR
NON-VOLATILE MEMORY DEVICE**
Filing Date: 12/04/2003
Atty. Docket No. **843.43311X00**

From: Larry N. Anagnos

Fax Number: (571) 273-2885

Total Number of Pages: **3**

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